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storage same device and packing same integer and operand



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### 1 [Real-time shading](#)



Marc Olano, Kurt Akeley, John C. Hart, Wolfgang Heidrich, Michael McCool, Jason L. Mitchell, Randi Rost

 August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**
**Publisher:** ACM Press

 Full text available: [pdf\(7.39 MB\)](#)

 Additional Information: [full citation](#), [abstract](#)

Real-time procedural shading was once seen as a distant dream. When the first version of this course was offered four years ago, real-time shading was possible, but only with one-of-a-kind hardware or by combining the effects of tens to hundreds of rendering passes. Today, almost every new computer comes with graphics hardware capable of interactively executing shaders of thousands to tens of thousands of instructions. This course has been redesigned to address today's real-time shading capabili ...

### 2 [Proceedings of the SIGNUM conference on the programming environment for development of numerical software](#)


 March 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue 1

**Publisher:** ACM Press

 Full text available: [pdf\(5.02 MB\)](#)

 Additional Information: [full citation](#)

### 3 [Revised Report of the Algorithmic Language Algol 68](#)



A. van Wijngaarden

 August 1981 **ALGOL Bulletin**, Issue Sup 47

**Publisher:** Computer History Museum

 Full text available: [pdf\(9.20 MB\)](#)

 Additional Information: [full citation](#), [index terms](#)

### 4 [Fortran 8X draft](#)



Loren P. Meissner

 December 1989 **ACM SIGPLAN Fortran Forum**, Volume 8 Issue 4

**Publisher:** ACM Press

 Full text available: [pdf\(21.36 MB\)](#)

 Additional Information: [full citation](#), [abstract](#), [index terms](#)

**Standard Programming Language Fortran.** This standard specifies the form and establishes the interpretation of programs expressed in the Fortran language. It consists of the specification of the language Fortran. No subsets are specified in this standard. The previous standard, commonly known as "FORTRAN 77", is entirely contained within this standard, known as "Fortran 8x". Therefore, any standard-conforming FORTRAN 77 program is standard conforming under this standard. New features can b ...

5 Draft Report on the Algorithmic Language ALGOL 68

A. Van Wijngaarden, B. J. Mailloux, J. Peck, C. H. A. Koster  
March 1968 **ALGOL Bulletin**, Issue Sup 26


**Publisher:** Computer History Museum

Full text available:  [pdf\(5.16 MB\)](#) Additional Information: [full citation](#), [index terms](#)

6 Register Packing: Exploiting Narrow-Width Operands for Reducing Register File Pressure


Oguz Ergin, Deniz Balkan, Kanad Ghose, Dmitry Ponomarev  
December 2004 **Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture MICRO 37**

**Publisher:** IEEE Computer Society

Full text available:  [pdf\(224.06 KB\)](#) Additional Information: [full citation](#), [abstract](#)


A large percentage of computed results have fewer significant bits compared to the full width of a register. We exploit this fact to pack multiple results into a single physical register to reduce the pressure on the register file in a superscalar processor. Two schemes for dynamically packing multiple "narrow-width" results into partitions within a single register are evaluated. The first scheme is conservative and allocates a full-width register for a computed result. If the computed result tu ...

7 GPGPU: general purpose computation on graphics hardware

 David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn


August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes GRAPH '04**

**Publisher:** ACM Press

Full text available:  [pdf\(63.03 MB\)](#) Additional Information: [full citation](#), [abstract](#)

The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex and pixel processing units that support vector operations up to full IEEE floating point precision. High level languages have emerged for graphics hardware, making this computational power accessible. Architecturally, GPUs are highly parallel s ...

8 A Tutorial on Algol 68


 Andrew S. Tanenbaum

June 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 2

**Publisher:** ACM Press


Full text available:  [pdf\(2.92 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

9 A draft proposal for PASCAL

 A. M. Addyman


April 1980 **ACM SIGPLAN Notices**, Volume 15 Issue 4

**Publisher:** ACM Press

Full text available:  [pdf\(3.87 MB\)](#)


Additional Information: [full citation](#), [citations](#)

10 [A specification of JOVIAL](#)

 Christopher J. Shaw


December 1963 **Communications of the ACM**, Volume 6 Issue 12

**Publisher:** ACM Press

Full text available:  [pdf\(1.93 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#)

11 [The architecture of the SPERRY UNIVAC 1100 series systems](#)

 B. R. Borgerson, M. D. Godfrey, P. E. Hagerty, T. R. Rykken

April 1979 **Proceedings of the 6th annual symposium on Computer architecture**


**Publisher:** ACM Press

Full text available:  [pdf\(841.19 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


This paper presents an overview of the architecture of the SPERRY UNIVAC® 1100 Series systems. The principal topics are instruction and data formats, main storage and addressing, process management, and I/O.

12 [Pipeline Architecture](#)

 C. V. Ramamoorthy, H. F. Li


March 1977 **ACM Computing Surveys (CSUR)**, Volume 9 Issue 1

**Publisher:** ACM Press

Full text available:  [pdf\(3.53 MB\)](#)


Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

13 [A Sampler of Formal Definitions](#)

 Michael Marcotty, Henry Ledgard, Gregor V. Bochmann


June 1976 **ACM Computing Surveys (CSUR)**, Volume 8 Issue 2

**Publisher:** ACM Press

Full text available:  [pdf\(4.56 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

14 [Taming the IXP network processor](#)

 Lal George, Matthias Blume

May 2003 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2003 conference on Programming language design and implementation PLDI '03**, Volume 38 Issue 5

**Publisher:** ACM Press

Full text available:  [pdf\(159.27 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

We compile Nova, a new language designed for writing network processing applications, using a back end based on integer-linear programming (ILP) for register allocation, optimal bank assignment, and spills. The compiler's optimizer employs CPS as its intermediate representation; some of the invariants that this IR guarantees are essential for the formulation of a practical ILP model. Appel and George used a similar ILP-based technique for the IA32 to decide which variables reside in registers but ...

**Keywords:** Intel IXA, bank assignment, code generation, integer linear programming,

network processors, programming languages, register allocation

- 15 Energy-performance trade-offs for spatial access methods on memory-resident data  
Ning An, Sudhanva Gurumurthi, Anand Sivasubramaniam, Narayanan Vijaykrishnan, Mahmut Kandemir, Mary Jane Irwin

November 2002 **The VLDB Journal — The International Journal on Very Large Data**

**Bases**, Volume 11 Issue 3

**Publisher:** Springer-Verlag New York, Inc.

Full text available:  [pdf\(641.55 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

The proliferation of mobile and pervasive computing devices has brought energy constraints into the limelight. Energy-conscious design is important at all levels of system architecture, and the software has a key role to play in conserving battery energy on these devices. With the increasing popularity of spatial database applications, and their anticipated deployment on mobile devices (such as road atlases and GPS-based applications), it is critical to examine the energy implications of spatial ...

**Keywords:** Energy optimization, Multidimensional indexing, Resource-constrained computing, Spatial data

- 16 A Portable Extended Precision Arithmetic Package and Library with Fortran Precompiler

W. T. Wyatt, D. W. Lozier, D. J. Orser

September 1976 **ACM Transactions on Mathematical Software (TOMS)**, Volume 2 Issue 3

**Publisher:** ACM Press


Full text available:  [pdf\(1.52 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

- 17 Technical reports

SIGACT News Staff

January 1980 **ACM SIGACT News**, Volume 12 Issue 1

**Publisher:** ACM Press

Full text available:  [pdf\(5.28 MB\)](#) Additional Information: [full citation](#)

- 18 The design of a virtual machine for Ada

L. J. Groves, W. J. Rogers

November 1980 **ACM SIGPLAN Notices , Proceeding of the ACM-SIGPLAN symposium on Ada programming language SIGPLAN '80**, Volume 15 Issue 11

**Publisher:** ACM Press

Full text available:  [pdf\(1.27 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

An implementation of Ada should be based on a machine-independent translator generating code for a Virtual Machine, which can be realised on a variety of machines. This approach, which leads to a high degree of compiler portability, has been very successful in a number of recent language implementation projects and is the approach which has been specified by the U. S. Army and Air Force in their requirements for Ada implementations. This paper discusses the rationale, requirements and design of s ...

- 19 Architecture of the Symbolics 3600


David A. Moon

June 1985 **ACM SIGARCH Computer Architecture News , Proceedings of the 12th**

**annual international symposium on Computer architecture ISCA '85**, Volume

13 Issue 3

**Publisher:** IEEE Computer Society Press, ACM Press

Full text available:  [pdf\(802.91 KB\)](#) Additional Information: [full citation](#), [citations](#), [index terms](#)


**20** [Experience with an extensible language](#)



Edgar T. Irons

January 1970 **Communications of the ACM**, Volume 13 Issue 1

**Publisher:** ACM Press

Full text available:  [pdf\(1.17 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

An operational extensible language system is described. The system and its base language are appraised with respect to efficiency, flexibility, and utility for different categories of users.

**Keywords:** ambiguity, bootstrapping, compiler, extensible, programming languages

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